

LISTING OF CLAIMS

Please **amend** the claims as follows:

1. (Canceled)

2. (Canceled)

3. (Currently amended) A display device panel assembly for a display device, ~~the panel assembly~~ comprising:

a first substrate;

a gate line formed on the first substrate;

a data line intersected with the gate line;

a second substrate facing the first substrate; and

a plurality of column spacers formed on at least one of the first and the second substrate,

a panel; and

a plurality of column spacers formed on the panel for supporting the panel;

wherein the spacers have at least two different heights or at least two different contact areas with the panel, wherein the spacers comprise a first spacer and a second spacer, and contact areas of the first and the second spacer are different, and at least one of the spacers has a tapered structure plurality of first spacers and a plurality of second spacers having a height lower than the first spacers and having a contact area wider than the first spacers.

4. (Currently amended) The display device panel assembly of claim 3, wherein a the height difference between the first spacers and the second spacer spacers is in a range of about 0.3-0.6 microns.

5. (Currently amended) The display device panel assembly of claim 3, wherein ~~the a height of the second spacer is lower than the first spacer second spacers have a length larger than the first spacers by 10-20 microns.~~

6. (Currently amended) The panel assembly display device of claim 3, wherein the second spacer spacers ~~has have~~ a length in a range of about 30-35 microns and the first spacer has spacers have a length in a range of about 15-20 microns.

7. (Currently amended) The ~~panel assembly~~ display device of claim 3, wherein a concentration of the second spacers is about 200-600/cm² and a concentration of the first spacer is about 250-450/cm².

Claims 8-12 (Canceled).

13. (Original) A method of manufacturing a liquid crystal panel assembly, the method comprising:

coating a photoresist on a panel;

light-exposing the photoresist through an exposure mask including an opening and disposed on the panel with a first distance;

light-exposing the photoresist through the exposure mask disposed on the panel with a second distance; and

developing the photoresist to form first and second spacers having different heights or different contact areas with the panel.

14. (Original) The method of claim 13, wherein the photoresist is a negative type.

Claims 15-21 (Canceled).

22. (Currently amended) A ~~panel assembly for a display device, the panel assembly comprising:~~

a first substrate;

a gate line formed on the first substrate;

a data line intersected with the gate line;

a second substrate facing the first substrate;

a blocking layer formed on at least one of the first and second substrate; and

a plurality of column spacers formed on at least one of the first and the second substrate,

wherein the spacers comprise a first spacer and a second spacer, and contact areas of the first and second spacers are different, and at least a portion of the spacers is overlapped with the blocking layer.

~~a panel; and~~

~~a plurality of column spacers formed on the panel for supporting the panel;~~

~~wherein the spacers have at least two different heights and at least two different lengths with the panel.~~

23. (Currently amended) The display device panel assembly of claim 22, wherein the a height of the second spacer is ~~spacers comprise a plurality of first spacers and a plurality of second spacers having a height lower than the~~ a height of the first spacers~~spacers and having a length longer than the first spacers.~~

24. (Currently amended) The display device panel assembly of claim 23, wherein the a height difference between the first spacers and the second spacer ~~spacers is~~ in a range of about 0.3-0.6 microns.

25. (Currently amended) The display device of claim 22 panel assembly of claim 23, wherein the a length difference between the first and the second spacer is in a range of about ~~spacers have a length larger than the first spacers by 10-20 microns.~~

26. (Currently amended) The ~~panel assembly display device~~ of claim ~~25~~ 23, wherein the second ~~spacer~~ ~~spacers~~ ~~has~~ ~~have~~ a length in a range of about 30-35 microns and the first ~~spacer~~ ~~has~~ ~~spacers~~ ~~have~~ a length in a range of about 15-20 microns.

27. (Currently amended) The ~~panel assembly display device~~ of claim ~~22~~ 23, wherein a concentration of the second ~~spacer~~ ~~spacers~~ is about 200-600/cm² and a concentration of the first spacer is about 250-450/cm².

28. (Currently amended) The ~~display device panel assembly~~ of claim 22, wherein the spacers further comprise a third spacer, and the second spacer has a height a first spacer, a second spacer having a height lower than a height of the first spacer, and a the third spacer has ~~having~~ a height equal to or lower than the height of the second spacer.

29. (Currently amended) The ~~panel assembly display device~~ of claim 28, wherein the height of the third spacer is equal to the height of the second spacer.

30. (Currently amended) The ~~display device panel assembly~~ of claim 22, wherein the first substrate panel comprises ~~a gate line and a data line transmitting electrical signals;~~ a thin film transistor electrically connected to the gate line and the data line, and a pixel electrode connected to the thin film transistor.

31. (Currently amended) The ~~display device panel assembly~~ of claim 22, wherein the second substrate panel comprises a plurality of color filters having different thicknesses.

Please **add** the following new claims:

32. (New) The display device of claim 3, wherein the second spacer has a length larger than the first spacer by 10-20 microns.

33. (New) The display device of claim 22, wherein at least one of the spacers has a tapered structure.

34. (New) The display device of claim 30, wherein at least a portion of the spacer is overlapped with at least a portion of the data line.

35. (New) A display device comprising:

a first substrate;

a gate line formed on the first substrate;

a data line intersected with the gate line;

a second substrate facing the first substrate;

a blocking layer formed on at least one of the first and the second substrate; and

a plurality of column spacers formed on at least one of the first and the second substrate,

wherein the spacers comprise a first spacer and a second spacer, and heights of the first and the second spacer are different, and at least a portion of the spacers is overlapped with the blocking layer.